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IN THE

UNITED STATES PATENT AND TRADEMARK OFFICE

า H. Cook III, et al.

Serial No.: 09/672650 Examiner: Shelly A. Chase

Filing Date: 9/28/00

Group Art Unit: 2133

Title:

Memory Tester Has Memory Sets Configurable For Use As Error Catch RAM, Tag RAM's,

Buffer Memories And Stimulus Log RAM

COMMISSIONER FOR PATENTS PO Box 1450 Alexandria, VA 22313-1450

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JUN 0 3 2004

PETITION FOR EXTENSION OF TIME

Technology Center 2100

Sir:

In an Office Action	on mailed on 1/22/04 , on the above-identified U.S. Patent
application, a shortened	statutory period of3 months was set for response. In accordance
with 37 C.F.R. 1.136(a), applicant(s) hereby request(s) a:	
(X)	one month
()	two months
()	three months
()	four months
time extension so that the period for response to the Office Action expires on 5/22/04	

Authorization to charge the fee required by 37 CFR 1.17 to Deposit Account 50-1078 appears in the enclosed transmittal letter. At any time during the pendency of this application, please charge any fees required or credit any overpayment to Deposit Account 50-1078 pursuant to 37 CFR 1.25.

I hereby certify that this correspondence is being deposited with the United States Postal Service as :

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in an envelope addressed to: Commissioner for Patents, PO Box 1450, Alexandria, VA 22313-1450.

Date of Deposit: May 24, 2004

Typed Name: Sue A. Hebert

he Atelet

Respectfully submitted,

John H. Cook III, et al.

Edward L. Miller

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Date: May 24, 2004

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